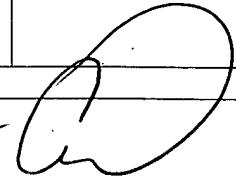


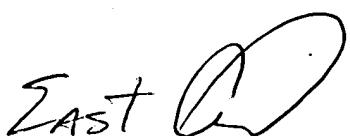
Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L2	145	("12 bits" or 12-bits or twelve bits) with (most significant or msb) and ("2 bits" or 2-bits or two bits) with (least significant or lsb)) or ("12 bits" or 12-bits or twelve bits) with (least significant or lsb) and ("2 bits" or 2-bits or two bits) with (most significant or msb) and (interleav\$3 or inter-leav\$3) and linear	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:53
L3	127	("12 bits" or 12-bits or twelve bits) with (most significant or msb) and ("2 bits" or 2-bits or two bits) with (least significant or lsb)) or ("12 bits" or 12-bits or twelve bits) with (least significant or lsb) and ("2 bits" or 2-bits or two bits) with (most significant or msb) with (interleav\$3 or inter-leav\$3) and linear	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:53
L4	73	("12 bits" or 12-bits or twelve bits) with (most significant or msb) and ("2 bits" or 2-bits or two bits) with (least significant or lsb)) or ("12 bits" or 12-bits or twelve bits) with (least significant or lsb) and ("2 bits" or 2-bits or two bits) with (most significant or msb) with (interleav\$3 or inter-leav\$3) with linear	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:54
L5	123	("12 bits" or 12-bits or twelve bits) with (most significant or msb) and ("2 bits" or 2-bits or two bits) with (least significant or lsb)) or ("12 bits" or 12-bits or twelve bits) with (least significant or lsb) and ("2 bits" or 2-bits or two bits) with (most significant or msb) with (interleav\$3 or inter-leav\$3) with linear with (memory bank or memory module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:55
L6	91	("12 bits" or 12-bits or twelve bits) with (most significant bit or msb) and ("2 bits" or 2-bits or two bits) with (least significant bit or lsb)) or ("12 bits" or 12-bits or twelve bits) with (least significant bit or lsb) and ("2 bits" or 2-bits or two bits) with (most significant bit or msb) with (interleav\$3 or inter-leav\$3) with linear with (memory bank or memory module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:56

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L7	12	(("12 bits" or 12-bits or twelve bits) with (most significant bit or msb) with ("2 bits" or 2-bits or two bits) with (least significant bit or lsb)) or (("12 bits" or 12-bits or twelve bits) with (least significant bit or lsb) with ("2 bits" or 2-bits or two bits) with (most significant bit or msb)) with (interleav\$3 or inter-leav\$3) with linear with (memory bank or memory module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:57
L8	18	(("10 bits" or 10-bits or ten bits) with (most significant bit or msb) with ("2 bits" or 2-bits or two bits) with (least significant bit or lsb)) or (("10 bits" or 10-bits or ten bits) with (least significant bit or lsb) with ("2 bits" or 2-bits or two bits) with (most significant bit or msb)) with (interleav\$3 or inter-leav\$3) with linear with (memory bank or memory module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:58
L9	1258	("2 bits" or 2-bits or two bits) with (most significant bit or msb or lsb or least significant bit) with (bank id or bank number or bank select) and interleav\$3 or inter-leav\$3	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 12:00
L17	378	(address counter) same (interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 12:09
L18	52	(address counter) same (interleav\$3 or inter-leav\$3) same linear	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 12:09
L19	74	(single port RAM or single-port ram or single port memor\$3 or single-port memor\$3) with (benefit or advantag\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 12:23
L20	22	(single port RAM or single-port ram or single port memor\$3 or single-port memor\$3) near4 (benefit or advantag\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 12:35

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L21	5	(single port RAM or single-port ram or single port memor\$3 or single-port memor\$3) near4 (benefit or advantag\$4) and (interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 12:30
L23	65	(single port RAM or single-port ram or single port memor\$3 or single-port memor\$3) near4 (cost or speed or fast\$2)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 12:37
L25	10	(single port RAM or single-port ram or single port memor\$3 or single-port memor\$3) near4 (cost or speed or fast\$2) and (interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 12:40
L34	124	((interleav\$3 or inter-leav\$3) and linear) with (switch\$3 or swap\$4 or chang\$3) and (IC or single chip or integrated circuit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 13:03
L40	136	(single IC or single chip or single integrated circuit) and (interleav\$3 or inter-leav\$3) same (linear)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 14:36
L42	1354	(single IC or single chip or single integrated circuit) and communication and linear and (interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 14:37
L43	32	(single IC or single chip or single integrated circuit) and communication controller and linear and (interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 14:39
L44	599	(single IC or single chip or single integrated circuit) near3 (benefit or advantag\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 14:40



L46	27	(single IC or single chip or single integrated circuit) near3 (benefit or advantag\$4) and (interleav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 14:43
L47	55	(single IC or single chip or single integrated circuit) near3 (benefit or advantag\$4) and (memory array)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 14:43
L48	5	("10 address bits" or ten address bits) same ("2 address bits" or two address bits)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:02
L49	13	("12 address bits" or twelve address bits) same ("2 address bits" or two address bits)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:05
L50	907	("12 bits" or twelve bits) with ("2 bits" or two bits)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:05
L51	47	("12 bits" or twelve bits) with ("2 bits" or two bits) near4 (memory or bank or module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:07
L52	45	("10 bits" or ten bits) with ("2 bits" or two bits) near4 (memory or bank or module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:09
L53	8	("10 bit address" or ten bit address) with ("2 bit" or two bit) near4 (memory or bank or module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:11

L54	21	("12 bit" or twelve bit) near4 address with ("2 bit" or two bit) near4 (memory or bank or module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:14
L55	8	("12 bit" or twelve bit) address with ("2 bit" or two bit) near4 (memory or bank or module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:19
L58	8	("10 bit" or ten bit) near4 (offset or address) with ("2 bit" or two bit) near4 (memory or bank or module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:26
L59	101	("10 bit" or ten bit) with ("2 bit" or two bit) with ("12 bit" or twelve bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:27
L60	7	("10 bit" or ten bit) with ("2 bit" or two bit) with ("12 bit" or twelve bit) with (memory or bank or module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 15:33
L61	6	("10 bit" or ten bit) same ("12 bit" or twelve bit) same ("2 bit" or two bit) near4 (memory or bank or module)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 16:01
L66	147	("2 bit" or two bit) near4 (memory or bank or module) near3 select	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 16:02
L69	29	("2 bit" or two bit) near4 (memory or bank or module) same ("10 bit" or ten bits) near2 address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 16:03
S1	1	"20040168011"	US-PGPUB	ADJ	ON	2005/07/13 08:51

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S2	2	"6392572".pn.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/12 15:14
S3	647	711/157	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 08:52
S4	267	((linear and interleav\$3) with access\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 09:01
S5	11	S3 and S4	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 08:52
S6	885	((linear or sequential or serial or consecutive) near6 access\$3) and (interleav\$3 near6 access\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 09:05
S7	67	S3 and S6	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 09:03
S8	345	((((linear or sequential or serial or consecutive) near6 access\$3) and (interleav\$3 near6 access\$3)) same address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 11:27
S9	34	S3 and S8	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 11:17

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S10	1619	711/5	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 11:17
S11	38	S8 and S10	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 11:34
S12	16	S11 not S9	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 11:17
S13	43	((linear or sequential or serial or consecutive) near3 (order or access\$3)) and ((interleav\$3 near3 (order or access\$3))) same address same (switch\$3 or chang\$3 or swap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 12:04
S14	177	((linear\$2 or sequential\$2 or serial\$2) near3 (order\$3 or access\$3 or mode)) same ((interleav\$3) near3 (order\$3 or access\$3 or mode)) and parallel and (address with (change\$3 or switch\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 15:10
S15	4	((linear\$2 or sequential\$2 or serial\$2) near3 (order\$3 or access\$3 or mode)) same ((interleav\$3) near3 (order\$3 or access\$3 or mode)) same parallel same (address with (change\$3 or switch\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 15:11
S16	63	((linear\$2 or sequential\$2 or serial\$2) near3 (order\$3 or access\$3 or mode)) same ((interleav\$3) near3 (order\$3 or access\$3 or mode)) same (address with (change\$3 or switch\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 15:11
S17	43	((linear or sequential or serial or consecutive) near3 (order or access\$3)) and ((interleav\$3 near3 (order or access\$3))) same address same (switch\$3 or chang\$3 or swap\$4)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 15:11

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S18	37	S16 not S17	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 15:12
S19	58	linear with (interleav\$3 or inter-leav\$3) with (switch\$3 or swap\$4 or chang\$3) same (address or identifier or order or mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 16:00
S20	176	(sequential\$2 or serial\$2) with (interleav\$3 or inter-leav\$3) with (switch\$3 or swap\$4 or chang\$3) same (address or identifier or order or mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 15:59
S21	66	(sequential\$2 or serial\$2) with (interleav\$3 or inter-leav\$3) with (switch\$3 or swap\$4 or chang\$3) same (address or identifier) same (order or mode)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 16:03
S22	93	linear with (interleav\$3 or inter-leav\$3) with (switch\$3 or swap\$4 or chang\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/14 12:26
S23	35	S22 not S19	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 16:25
S25	24	linear with (interlac\$3 or inter-lac\$3) with (switch\$3 or swap\$4 or chang\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/13 16:01
S27	231	peugh.xa. or peugh.xp.	US-PGPUB; USPAT	ADJ	ON	2005/07/14 12:08
S28	35747	brian.xa. or brian.xp.	US-PGPUB; USPAT	ADJ	ON	2005/07/14 12:08
S29	175	S27 and S28	US-PGPUB; USPAT	ADJ	ON	2005/07/14 12:09
S31	5	S29 and geometry	US-PGPUB; USPAT	ADJ	ON	2005/07/14 12:11

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S32	4	S29 and (horizontal or verticle)	US-PGPUB; USPAT	ADJ	ON	2005/07/14 12:12
S33	1	S29 and (horizontal or verticle) and (interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT	ADJ	ON	2005/07/14 12:12
S34	98	linear with (interleav\$3 or inter-leav\$3) with (shift\$3 or rotat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/14 16:05
S35	93	linear with (interleav\$3 or inter-leav\$3) with (switch\$3 or swap\$4 or chang\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/14 12:28
S38	50	linear with (interleav\$3 or inter-leav\$3) with (switch\$3 or swap\$4 or chang\$3) same (address or identifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/14 12:29
S41	17	linear with (interleav\$3 or inter-leav\$3) with (shift\$3 or rotat\$3) same (address or identifier)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/14 12:35
S42	6554	(lsb or least significant bit) with (shift\$3 or rotat\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/14 12:37
S46	120	(lsb or least significant bit) with (shift\$3 or rotat\$3) with (linear or interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/14 12:48
S47	38	(msb or most significant bit) with (shift\$3 or rotat\$3) with (linear or interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 08:58
S48	19	S46 and S47	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/14 12:48

S49	12	linear with (interleav\$3 or inter-leav\$3) with (shift\$3 or rotat\$3) with address	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/14 16:07
S54	487	(msb or most significant bit) with (linear or (interleav\$3 or inter-leav\$3))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 08:59
S55	31	(msb or most significant bit) with (linear or (interleav\$3 or inter-leav\$3)) with (change or swap\$4 or switch\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 09:19
S56	40	(lsb or least significant bit) with (linear or (interleav\$3 or inter-leav\$3)) with (change or swap\$4 or switch\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 10:59
S57	28	S56 not S55	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 09:20
S58	3791	(10-bits or "10 bits" or ten bits) and (2-bits or two bits or four memor\$3) and (linear or interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:01
S59	7	(10-bits or "10 bits" or ten bits) with (2-bits or two bits or four memor\$3) with (linear or interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:02
S62	29	(12-bits or "12 bits" or twelve bits) with (2-bits or two bits or "2 bits" or four memor\$3 or "4 memor\$3") same (linear or interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 14:59



S63	89	(12-bits or "12 bits" or twelve bits) with (2-bits or two bits or "2 bits" or four memor\$3 or "4 memor\$3") and linear and (interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:06
S64	58	(12-bits or "12 bits" or twelve bits) with (2-bits or two bits or "2 bits" or four memor\$3 or "4 memor\$3") and linear and (interleav\$3 or inter-leav\$3) and (lsb or least significant bit or msb or most significant bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:11
S65	27	(10-bits or "10 bits" or ten bits) with (2-bits or two bits or "2 bits" or four memor\$3 or "4 memor\$3") same (linear or interleav\$3 or inter-leav\$3) and (lsb or least significant bit or msb or most significant bit)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:12
S67	657	((advantag\$3 or benefit) near3 (RAM or random access memory)).bsum.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:19
S68	141	(single port RAM or single-port RAM or single port random access memory or single-port random access memory).bsum.	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:20
S72	71	(advantag\$3 or benefit) near3 (single port or single-port)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:23
S73	16	(advantag\$3 or benefit) near3 (single port memory or single-port memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:24
S76	64	(single port memory or single-port memory or single port RAM or single-port RAM) same (interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:27



S78	25971	(IC or chip or integrated circuit) and (interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:36
S79	10509	(IC or chip or integrated circuit) and (interleav\$3 or inter-leav\$3) and (RAM or random access memory)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:37
S80	4015	(IC or chip or integrated circuit) and (interleav\$3 or inter-leav\$3) and (RAM or random access memory) and (linear)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:37
S81	1172	(single IC or single chip or single integrated circuit) and (interleav\$3 or inter-leav\$3) and (RAM or random access memory) and (linear)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:37
S83	239	(("10 bits" or 10-bits or ten bits) with (most significant or msb) and ("2 bits" or 2-bits or two bits) with (least significant or lsb)) or (("10 bits" or 10-bits or ten bits) with (least significant or lsb) and ("2 bits" or 2-bits or two bits) with (most significant or msb))	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:47
S84	170	(("10 bits" or 10-bits or ten bits) with (most significant or msb) and ("2 bits" or 2-bits or two bits) with (least significant or lsb)) or (("10 bits" or 10-bits or ten bits) with (least significant or lsb) and ("2 bits" or 2-bits or two bits) with (most significant or msb)) and (interleav\$3 or inter-leav\$3)	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:47
S85	131	(("10 bits" or 10-bits or ten bits) with (most significant or msb) and ("2 bits" or 2-bits or two bits) with (least significant or lsb)) or (("10 bits" or 10-bits or ten bits) with (least significant or lsb) and ("2 bits" or 2-bits or two bits) with (most significant or msb)) and (interleav\$3 or inter-leav\$3) and linear	US-PGPUB; USPAT; USOCR; EPO; JPO; DERWENT; IBM_TDB	ADJ	ON	2005/07/18 11:52



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linear and (interleav* or inter-leav*) and (switch* or chang* or swap* or rotat* or shift*)
7/14/05; 63 hits

ACM Search

+abstract:linear +abstract:interleav* abstract:switch* abstract:swap*
abstract:rotat* abstract:shift*
7/14/05; 10 hits

AM